Multiple-Edge-Fault Tolerance with Respect to Hypercubes

Ting-Yi Sung, Men-Yang Lin, and Tung-Yang Ho

Abstract—Previous works on edge-fault tolerance with respect to hypercubes Q_n are mainly focused on 1-edge fault and 2- or 3edge fault with limited size of *n*. We give a construction scheme for 2-EFT(Q_n) graphs and 3-EFT(Q_n) graphs, where *n* is arbitrarily large. In our constructions, approximately *log n* extra degree is added to the vertices of Q_n for 2-edge-fault tolerance, and one more degree for 3-edge-fault tolerance.

Index Terms—Edge-fault tolerance, reconfigurations, hypercubes, interconnection networks, linear algebra, vector space.

1 INTRODUCTION

 \mathbf{V} E usually use a graph to represent the architecture of an interconnection and the architecture of the second seco an interconnection network, where nodes represent processors and edges represent communication links between pairs of processors. Faults may occur in nodes and/or edges of an interconnection network. We restrict faults to edges only in this paper. We first formally define edge-fault tolerance in graph terminology, which was proposed by Harary and Hayes [4]. Let $G_1 = (V, E_1)$ be a graph and $G_2 = (V, E_2)$ be a subgraph of G_1 . We use $G_1 - G_2$ or $G_1 - E_2$ to denote the graph obtained from G_1 by removing all edges in E_2 . Given a *target graph* H=(V, E), let G=(V, E')be a supergraph of H. G is said to be k-edge-fault-tolerant with respect to H, denoted by k-EFT(H), if for any $F \subseteq E'$ and |F| = k, G - F contains a subgraph isomorphic to H, which is called a reconfiguration for k-edge fault F (or simply a reconfiguration). A reconfiguration can be viewed as a relabeling of vertices of G such that G - F contains H. The graph G^* is said to be *optimal* if G^* contains the smallest number of edges among all k-EFT(H) graphs. The target graph always represents some popular interconnection network for which many communication softwares are available. Extra edges are added to the target graph such that when edge faults occur, the network can be reconfigured to obtain the target graph.

Meshes and hypercubes are important network topologies. Edge-fault tolerant graphs with respect to meshes and hypercubes have been studied in [1], [2], [4], [6], [7]. In particular, let Q_n be an *n*-dimensional hypercube. An optimal 1-EFT(Q_n) graph G^* has been proposed in [1], [4], [7] which is given by adding to Q_n the set of edges $\{(v, \overline{v}) | \text{ for all } v\}$, where each entry in \overline{v} is the complement of that in *v*. This 1-EFT(Q_n) graph is called *folded hypercube* in [3]. Since we can construct optimal 1-EFT(Q_n) graphs G^* , the question

Manuscript received Jan. 19, 1995; revised Nov. 1, 1995. For information on obtaining reprints of this article, please send e-mail to: transpds@computer.org, and reference IEEECS Log Number D95264. whether *k*-EFT(Q_n) graphs for $k \ge 2$ can be derived from G^* naturally arises. In this paper, we study multiple-edge-fault tolerance with respect to hypercube Q_n .

Bruck et al. [1] used a vector-space approach to developing 1-EFT (Q_n) graphs with one additional wildcard dimension and used techniques from error-correcting codes to add wildcard dimensions to meshes and tori for edge-fault tolerance. They also showed that only a single wildcard dimension can be added to hypercubes. This implies that we cannot apply directly the concept of wildcard dimension to construct *k*-EFT(Q_n) graphs for $k \ge 2$. Shih and Batcher [6] employed vector-space concept and presented necessary conditions of a so-called redundancy matrix for constructing a k-EFT(Q_n) graph, which is given by the adjacency relationship of the additional edges to Q_n (Theorem 1 in [6]). They developed an ad hoc program to generate a matrix satisfying the necessary conditions and verify its sufficiency for edge-fault tolerance. They could construct k-EFT(Q_n) graphs for only k = 2, 3 and $n \le 26$ due to tremendous computation effort required in this program. We, instead, give an analytical scheme for the construction of 2-EFT(Q_n) graphs and 3-EFT(Q_n) graphs, where n is arbitrarily large. In our constructions, the 3-EFT(Q_n) graph has exactly one more edge for each vertex than in the 2-EFT(Q_n) graph. Approximately, log n extra degree is added to Q_n for 2-edge-fault tolerance.

2 DEFINITIONS AND NOTATION

A hypercube of dimension *n*, denoted by $Q_n = (V, E)$, is an undirected graph consisting of 2^n vertices and $n2^{n-1}$ edges. Each vertex *v* has an *n*-bit *binary* address $v = (v_1, v_2, ..., v_n)$, where v_i is called the *i*th bit of *v*. We use \overline{v}_i to denote the complement of v_i . A vertex is called the *antipodal* of *v*, denoted by \overline{v} , if $\overline{v} = (\overline{v}_1, \overline{v}_2, ..., \overline{v}_n)$. We use *v* to denote a vertex and its corresponding row vector for binary address interchangeably. Two vertices are linked by an edge if and only if their binary addresses differ in exactly one bit. Every vertex has degree *n*. An edge (u, v) is called a *dimension-i*

T.-Y. Sung and T.-Y. Ho are with the Institute of Information Science, Academia Sinica, Taipei, Taiwan, R.O.C. E-mail: tsung@iis.sinica.edu.tw.

[•] M.-Y. Lin is with the Department of Information Management, National Taichung Institute of Commerce, Taichung, Taiwan, R.O.C.

edge or simply an *i*th *edge*, $1 \le i \le n$, if the binary addresses for *u* and *v* differ in the *i*th bit. Furthermore, *u* is called the *i*th *neighbor* of *v*, denoted by $N_i(v) = u$. Equivalently, we have $N_i(u) = v$. We define $N(v) = \{N_1(v), N_2(v), ..., N_n(v)\}$. Furthermore, we also use N(v) to denote the $n \times n$ matrix with row *i* corresponding to $N_i(v)$.

Given a *k*-EFT(Q_n) graph *G*, the set of edges added to Q_n is denoted by *A*. We use A(v) to denote the set of *neighbors* adjacent to *v* which constitute edges in *A*. A(v) is also used to represent the matrix with each row corresponding to a neighbor in A(v). In a specific reconfiguration R(F) for a *k*-edge fault *F*, the neighbors of *v* are denoted by $R_1(v)$, $R_2(v)$, ..., $R_n(v)$. Moreover, we use R(v) to denote the set of $R_1(v)$, $R_2(v)$, ..., $R_n(v)$ and the $n \times n$ matrix with the *i*th row corresponding to $R_i(v)$ interchangeably. Take Q_3 and an arbitrary 1-edge fault $F = \{(001, 101)\}$ as an example. We illustrate 1-EFT(Q_3) and R(F) in Fig. 1. (In Fig. 1, dotted lines represent edges in *A*.) For v = (111), R(v) is given by $\{(000, 111), (101, 111), (110, 111)\}$ or





Fig. 1. For Q_3 : (a) An optimal 1-EFT(Q_3) graph. (b) A reconfiguration for 1-edge fault $F = \{(001, 101)\}$.

For a matrix M, the *i*th row and the *j*th column of M are denoted by M_i and M^j , respectively. We use $M[i_1 \dots i_k, j_1 \dots j_l]$ to denote the $k \times l$ submatrix of M that consists of rows i_1 ,

..., i_k and columns $j_1, ..., j_l$. For example, the 2×2 submatrix of A(v) consisting of rows i_1 , i_2 and columns j_1 , j_2 is denoted by $A[i_1i_2, j_1j_2](v)$ (rather than $A(v)[i_1i_2, j_1j_2]$ for clarity). The entry located at the *i*th row and *j*th column, M[i, j], is written as M_{ij} .

All matrix and vector operations in this paper are defined over Galois Field *GF*(2). Let v_1 and v_2 be two (0, 1)-vectors. The number of 1s in v_1 is called the *Hamming weight* of v_1 . The number of entries that are different in v_1 and v_2 is called the *Hamming distance* between v_1 and v_2 . All edges in Q_n have a Hamming distance of 1.

3 CONSTRUCTION OF A *k*-EFT(*Q_n*) GRAPH

Consider a hypercube Q_n . Let v^0 denote the node (0, ..., 0) henceforth. $N(v^0)$ is an identity matrix of size n, denoted by I_n . Let (u, v) be an *i*th edge. It follows that $u - v = e_i$ where e_i is the *i*th unit vector. Therefore, $N(v^0)$ can also represent address differences of all the edges incident at any specific vertex. In other words,

$$N(v) = \underline{1} \cdot v + N(v^0) \quad \text{for all vertices } v, \qquad (1)$$

where $\underline{1}$ is a column vector of 1s. Note that $N(v^0)$ is invertible, i.e., rows in $N(v^0)$ span $GF(2)^n$. It follows from (1) that $N(v) - \underline{1} \cdot v$ is also invertible, i.e., rows in $N(v) - \underline{1} \cdot v$ span $GF(2)^n$. In this paper, we require in all reconfigurations for edge faults, $R(v) - \underline{1} \cdot v$ for each vertex has the same *full rank* property as in Q_n . To be specific, we aim at constructing *k*-EFT(Q_n) graphs such that in all reconfigurations, rows in $R(v^0)$ span $GF(2)^n$, and

$$R(v) = \underline{1} \cdot v + R(v^{0}) \qquad \text{for all vertices } v. \qquad (2)$$

In this manner, $R(v^0)$ in a reconfiguration can represent address differences of the edges incident at any specific vertex. Thus, it is essential to find an **invertible** $R(v^0)$.

Based on the above discussion, we construct k-EFT(Q_n) graphs using the following relationship:

$$A(v) = A(v^0) + \underline{1} \cdot v \qquad \text{for all } v. \tag{3}$$

Our construction scheme always yields *vertex-symmetric* k-EFT(Q_n) graphs. To find an appropriate $A(v^0)$, it is required that after exclusion of k arbitrary rows from $\begin{bmatrix} N(v^0) \\ A(v^0) \end{bmatrix}$,

there exists an invertible $n \times n$ submatrix $R(v^0)$. Our construction and verification of *k*-EFT(Q_n) graphs proceed as follows:

Step1: Construct $A(v^0)$.

Step2: Show that after exclusion of *k* arbitrary rows from $\left[N(v^0) \right]$

 $\begin{bmatrix} I^{N(V)} \\ A(v^0) \end{bmatrix}$, there exists an invertible $n \times n$ submatrix $R(v^0)$.

Step3: A reconfiguration can be obtained according to (2).

We first state necessary conditions for $A(v^0)$ when an invertible $R(v^0)$ exists in reconfigurations for *k*-edge faults with $k \ge 2$. These necessary conditions can also be found

in Theorem 1 of [6]; we include the proof for the sake of completeness.

- LEMMA 1. When there exists an invertible $R(v^0)$, $A(v^0)$ must satisfy the following necessary conditions:
 - (C-1) the Hamming distance between any two columns is at least *k* 1, and
 - (C-2) each column has a Hamming weight at least k.
- PROOF. Suppose that the Hamming distance between two columns of $A(v^0)$, say $A^i(v^0)$ and $A^j(v^0)$, is k - 2. Let i_1, i_2, \dots, i_{k-2} be the rows that $A^i(v^0)$ and $A^j(v^0)$ differ. Let F_1 be a k-edge fault given by $F_1 = \{(v^0, N_i(v^0)), v^0\}$ $(v^0, N_i(v^0)) \} \cup \{(v^0, A_i(v^0)) \mid 1 \le p \le k - 2\}.$ Removing these *k* – 2 rows from $A(v^0)$, column *i*, and column *j* in the reduced matrix become identical. Thus, the reconfiguration for F_1 fails since we can not obtain an invertible $R(v^0)$. Suppose that there is a column of $A(v^0)$, say $A^{j}(v^{0})$, having only k - 1 entries of ones. These k - 11 entries are denoted by $A_{i,i}(v^0)$, $1 \le l \le k - 1$. Consider a *k*-edge fault $F_2 = \{(v^0, N_i(v^0))\} \cup \{(v^0, A_i^{(v)})) \mid 1 \le p$ $\leq k - 2$. The reconfiguration for F_2 fails since $R^{j}(v^{0})$ in any reconfiguration is a zero vector which is a contradiction. Hence, the lemma follows.

Necessary conditions provide guideline for finding an appropriate $A(v^0)$ which is sufficient for the existence of an invertible $R(v^0)$. In the following analysis, for a positive integer r, we define $m = \sum_{i=2}^{r} {r \choose i}$ which denotes the maximum dimension n of Q_n such that extra degree r suffices to construct 2-EFT(Q_n) and 3-EFT(Q_n) graphs. Next, we define an $r \times m$ matrix M_r as follows: Each column is distinct from others, and each of columns $\sum_{i=2}^{l-1} {r \choose i} + 1$ to $\sum_{i=2}^{l} {r \choose i}$ contains exactly l entries of 1s, where $2 \le l \le r$, and by convention $\sum_{i=2}^{l-1} {r \choose i} = 0$ for l = 2. To be specific, we illustrate M_r for r = 4. When r = 4, we have m = 11 and that M_4 is a 4×11 matrix given by

Note that each column of M_r has a Hamming weight at least 2 and that any two columns of M_r have a Hamming distance at least 1. Thus, M_r satisfies (C-1) and (C-2) with k = 2. For Q_n , we determine a **minimum** *r* such that

$$\sum_{r=2}^{r} \binom{r}{i} \ge n.$$
(4)

For example, when $5 \le n \le 11$, we have r = 4, and r = 5 for

 $12 \le n \le 26$.

From now on, we use *G* to denote the k-EFT(Q_n) graphs constructed according to (1) and (3).

4 A 2-EFT(Q_n) GRAPH

To construct a 2-EFT(Q_n) graph, we find $A(v^0)$ from M_r with r being appropriately chosen as stated before. We arbitrarily choose n columns of M_r to define $A(v^0)$. In this construction, r is the extra degree added to each vertex in Q_n . Since M_r satisfies (C-1) and (C-2), $A(v^0)$ also satisfies (C-1) and (C-2). On the other hand, we will show in the following lemma that $A(v^0)$ is sufficient for 2-edge-fault tolerance.

LEMMA 2. Let $A(v^0)$ be given as above. Then, after exclusion of two arbitrary rows from $\begin{bmatrix} N(v^0) \\ A(v^0) \end{bmatrix}$, there exists an $n \times n$ invertible submatrix $R(v^0)$.

PROOF. If the deleted rows are in $A(v^0)$, then $R(v^0) = N(v^0)$. Now consider that row *i* of $N(v^0)$ and row *j* of $A(v^0)$ are deleted. Choose a row $\alpha \neq j$ such that $A_{\alpha i}(v^0) = 1$; such a row α exists since $A^i(v^0)$ has a Hamming weight at least 2. Define

$$R(v^{0}) = \begin{bmatrix} I_{n-1} & 0\\ A'_{\alpha} & 1 \end{bmatrix}$$

after column permutation, where $A_{\alpha}(v^0)$ is written as $|A'_{\alpha}1|$ correspondingly. Thus, $R(v^0)$ is invertible.

Finally, consider two arbitrary rows, say rows i_1 and i_2 , are deleted from $N(v^0)$. Since $A(v^0)$ satisfies (C-1) and (C-2), we can always find two rows, say rows α_1 and α_2 , such that the 2 × 2 submatix $A[\alpha_1\alpha_2, i_1i_2](v^0)$ is invertible. Thus,

$$R(\mathbf{v}^{0}) = \begin{bmatrix} I_{n-2} & 0\\ A' & A[\alpha_{1}\alpha_{2}, i_{1}i_{2}](\mathbf{v}^{0}) \end{bmatrix}$$

is invertible, where

$$egin{bmatrix} A_{lpha_1}ig(\mathbf{v^0}ig)\ A_{lpha_2}ig(\mathbf{v^0}ig) \end{bmatrix}$$

is written as $\left[A' A[\alpha_1 \alpha_2, i_1 i_2](v^0)\right]$ after column permutation. This completes the proof.

Following the proof of Lemma 2, we can obtain reconfigurations for any 2-edge fault. Furthermore, R(v) - 1 + v spans $GF(2)^n$ for all v in these reconfigurations. Thus, we conclude the above discussion in the following theorem.

THEOREM 1. *G* is 2-*EFT*(Q_n) such that $R(v) - \underline{1} \cdot v$ spans $GF(2)^n$ for all *v* in reconfigurations for any 2-edge fault.

Since the extra degree *r* is the minimum integer satisfying (4), our proposed $A(v^0)$ contains the least number of

TABLE 1 EXTRA DEGREE IN 2-EFT(Q_n) GRAPHS

max n	1	4	11	26	57	120	247	502	1,013	2,036	4,083	8,178	16,369	32,752
r	2	3	4	5	6	7	8	9	10	11	12	13	14	15

rows (neighbors) under the proposed framework. We summarize in Table 1 the extra degree *r* in our constructed 2-EFT(Q_n) graphs for various *n*. It follows from the definition of the $r \times m$ matrix M_r that $m = \max n = 2^r - r - 1$. Therefore, we have the following remark.

REMARK. The extra degree r is approximately log n for Q_n and log n + 1 will suffice. (Actually, $r \approx \log n + \log \left(1 + \frac{\log n}{n}\right)$, to

be more precise.)

In the following, we use an example of a 2-EFT(Q_5) graph for illustration. It can be easily seen that r = 4, and we choose

$$A(v^0) = \begin{vmatrix} 1 & 1 & 1 & 0 & 0 \\ 1 & 0 & 0 & 1 & 1 \\ 0 & 1 & 0 & 1 & 0 \\ 0 & 0 & 1 & 0 & 1 \end{vmatrix}.$$

The proposed 2-EFT(Q_n) graph has the following four extra neighbors for each vertex v: v + (11100), v + (10011), v + (01010), and v + (00101), denoted by $A_1(v)$, $A_2(v)$, $A_3(v)$, and $A_4(v)$, respectively. We use the following faults to illustrate its 2-edge-fault tolerance.

- Let $F_1 = \{(11001, 11101), (10101, 10001)\}$ be a 2-edge fault. We can choose the edges $(v, A_1(v))$ or the edges $(v, A_4(v))$ to replace all of the third edges.
- Let $F_2 = \{(11001, 11101), (10111, 11111)\}$ be a 2-edge fault. We use the edges $(v, A_1(v))$ and $(v, A_3(v))$ to replace all of the second and the third edges.
- Let $F_3 = \{(11001, 11101), (00101, 11001)\}$ be a 2-edge fault. F_3 consists of a third edge and a $(v, A_1(v))$ edge. We use the edges $(v, A_4(v))$ to replace all of the third edges.

5 A 3-EFT(*Q*_n) GRAPH

Let T_{r+1} be an $(r + 1) \times m$ matrix, where

$$m = \sum_{i=2}^{r} \binom{r}{i}$$

given as follows:

$$T_{r+1} = \begin{bmatrix} M_r \\ t \end{bmatrix},$$

where *t* is a row vector with an entry of one if the corresponding column in M_r has an even Hamming weight, and zero otherwise. For example, T_5 is a 5 × 11 matrix given as follows:

The choice of *r* also follows as stated before. We add r + 1 edges to each vertex, and $A(v^0)$ is given by any *n* columns of T_{r+1} .

LEMMA 3. $A(v^0)$ satisfies (C-1) and (C-2) with k = 3.

PROOF. We first show T_{r+1} satisfies (C-1) and (C-2) with k = 3. For those columns of M_r having a Hamming weight of 2, the corresponding entries in vector t are 1s. Furthermore, since each column of M_r has a Hamming weight at least 2, each column of T_{r+1} has a Hamming weight at least 3. When two columns of M_r have a Hamming distance of 1, the two corresponding entries in t are different. Furthermore, since the Hamming distance between any two columns in M_r is at least 1, any two columns in T_{r+1} have a Hamming distance at least 2. Therefore, T_{r+1} satisfies (C-1) and (C-2) with k = 3. Since $A(v^0)$ also satisfies (C-1) and (C-2) with k = 3.

On the other hand, we will show in the next lemma that the construction of $A(v^0)$ is also sufficient for 3-edge-fault tolerance.

LEMMA 4. Let $A(v^0)$ be given as above. Then, after exclusion of at most three arbitrary rows from $\begin{bmatrix} N(v^0) \\ A(v^0) \end{bmatrix}$, there exists an $n \times n$ invertible submatrix $R(v^0)$.

- PROOF. Since $A(v^0)$ satisfies (C-1) and (C-2), the existence of an invertible $R(v^0)$ is obvious when those deleted rows contain at most two rows of $N(v^0)$. We need to consider only the case that exactly three rows of $N(v^0)$ are deleted. Let rows i_1 , i_2 , and i_3 be deleted from $N(v^0)$. Let $S(j) = \{i \mid A_{ij}(v^0) = 1 \text{ for } 1 \le i \le r\}$, and let S = $S(i_1) \cup S(i_2) \cup S(i_3)$. Since $A[S, i_1i_2i_3](v^0)$ is a submatrix of M_r and satisfies (C-1) and (C-2) with k = 2, it follows that $|S| \ge 3$. Furthermore, the $|S| \times 3$ submatrix $A[S, i_1i_2i_3](v^0)$ has rank of at least 2 since, otherwise, the three columns of $A[S, i_1i_2i_3](v^0)$ are identical which is a contradiction. We distinguish the following two cases:
 - 1) $A[S, i_1i_2i_3](v^0)$ has rank of three.

We simply choose any three rows from $A[S, i_1i_2i_3](v^0)$, say α_1 , α_2 , and α_3 , that are linearly independent.

Define

$$R(\mathbf{v}^{0}) = \begin{bmatrix} I_{n-3} & 0\\ A' & A[\alpha_{1}\alpha_{2}\alpha_{3}, i_{1}i_{2}i_{3}](\mathbf{v}^{0}) \end{bmatrix}$$

after column permutation. Since $A[S, i_1i_2i_3](v^0)$ is invertible, $R(v^0)$ is also invertible.

2) $A[S, i_1i_2i_3](v^0)$ has rank of two.

In this case, vector *t*, i.e., $A_{r+1}(v^0)$ must be incorporated with $A[S, i_1i_2i_3](v^0)$ to find an invertible $R(v^0)$. We first choose any two rows from $A[S, i_1i_2i_3](v^0)$, say α_1 and α_2 , that are linearly independent. We next show that

$$B = \begin{bmatrix} A \begin{bmatrix} \alpha_1 \alpha_2, i_1 i_2 i_3 \end{bmatrix} (v^0) \\ A \begin{bmatrix} r+1, i_1 i_2 i_3 \end{bmatrix} (v^0) \end{bmatrix}$$

is invertible. Since $A[S, i_1i_2i_3](v^0)$ has rank of 2, $A[S, i_1i_2i_3](v^0)$ can be reduced to a 3 × 3 matrix, called C, after eliminating identical rows. Since there is no zero row in C and each column of C has a Hamming weight at least 2, the matrix C has the form

$$\begin{bmatrix} 1 & 1 & 0 \\ 1 & 0 & 1 \\ 0 & 1 & 1 \end{bmatrix}$$

after column permutation. Therefore, each row in $A[S, i_1i_2i_3](v^0)$ has a Hamming weight of 2 since rows in $A[S, i_1i_2i_3](v^0)$ but not in C are identical to rows in *C*. Let $ST = \sum_{l=1}^{3} |S(i_l)|$. It follows that *ST* is even. That is, either all of $S(i_l)$ are even or two of $S(i_l)$ are odd and one is even. As a result A[r + 1, r] $i_1 i_2 i_3](v^0)$, is a row vector of ones in the former and a unit vector in the latter. In both cases, B is invertible. Define $R(v^0) = \begin{bmatrix} I_{n-3} & 0 \\ B' & B \end{bmatrix}$ after column permutation. Therefore, $R(v^0)$ is invertible. Hence, the lemma follows.

We conclude the above analysis in the following theorem.

THEOREM 2. G is 3-EFT(Q_n) such that $R(v) - \underline{1} \cdot v$ spans $GF(2)^n$ for all v in reconfigurations for any 3-edge fault.

 $A(v^{0})$ for 3-edge-fault tolerance is constructed from $A(v^{0})$ for 2-edge-fault tolerance with one additional edge for each vertex. Since the 2-EFT(Q_p) graph constructed in Section 4 contains the least number of edges under the proposed framework, the 3-EFT(Q_n) graph constructed in this section is also minimal in the same sense.

6 DISCUSSION

Constructions of *k*-EFT(Q_n) for $k \ge 4$ are still what we seek

for. Let A be an $r \times n$ submatrix of M_r . For ease of exposition, we choose *A* to be the first *n* columns of M_r . Let τ be a permutation on {1, 2, ..., *n*}. We define $A_1 = A$ and A_2 as the matrix obtained from A with columns arranged according to τ . Let D be a $2r \times n$ matrix given by $D = \begin{vmatrix} A_1 \\ A_2 \end{vmatrix}$, and $D' = \begin{bmatrix} D \\ t \end{bmatrix}$ where *t* is the row vector as defined for T_{r+1} in

Section 5. Let $S_1(j) = \{l \mid A_{li} = 1\}$ and $S_2(j) = \{l \mid A_{l\tau(j)} = 1\}$. We use Δ to denote the symmetric difference between two sets, e.g., $S_1(j_1) \Delta S_1(j_2) = (S_1(j_1) - S_1(j_2)) \cup (S_1(j_2) - S_1(j_1))$. To construct a 4-EFT(Q_n), we restrict τ to satisfy the following conditions.

- If $|S_1(j_1) \Delta S_1(j_2)| = 1$, then $|S_2(\tau(j_1)) \Delta S_2(\tau(j_2))| \ge 2$. If $|S_2(\tau(j_1)) \Delta S_2(\tau(j_2))| = 1$, then $|S_1(j_1) \Delta S_1(j_2)| \ge 2$.

It follows that *D* and *D'* satisfy (C-1) and (C-2) for k = 4 and k = 5, respectively. We have the following conjecture:

CONJECTURE 1. The graphs constructed according to $A(v^{0}) = D$ for k = 4 and $A(v^0) = D'$ for k = 5 are 4-EFT(Q_n) and 5-*EFT*(Q_n), respectively.

It is hard to show that D and D' are also sufficient for edgefault tolerance. Furthermore, characterizing such a permutation τ is also difficult.

ACKNOWLEDGMENTS

The authors would like to thank the anonymous referees for their helpful and constructive comments.

This work was partially supported by the National Science Council, Republic of China, under grant NSC-84-2213-E009-009.

REFERENCES

- J. Bruck, R. Cypher, and C.T. Ho, "Wildcard Dimensions, Coding [1] Theory, and Fault Tolerant Meshes and Hypercubes," IEEE Trans. Computers, vol. 44, no. 1, pp. 150-155, Jan. 1995.
- [2]R.S. Chou and L.H. Hsu, "1-Edge Fault-Tolerant Design for Meshes," Parallel Processing Letters, vol. 4, no. 4, pp. 385-389, 1994.
- A. El-Amawy and S. Latifi, "Properties and Performance of [3] Folded Hypercubes," IEEE Trans. Parallel and Distributed Systems, vol. 2, no. 1, pp. 31-42, Jan. 1991.
- [4] F. Harary and J.P. Hayes, "Edge Fault Tolerance in Graphs," Networks, vol. 23, pp. 135-142, 1993.
- M. O'Nan and H. Enderton, Linear Algebra, third edition. Orlando, [5] Fla.: Harcourt Brace Jovanovich, 1990.
- [6] C.J. Shih and K.E. Batcher, "Adding Multiple-Fault Tolerance to Generalized Cube Networks," IEEE Trans. Parallel and Distributed Systems, vol. 5, no. 8, pp. 785-792, Aug. 1994.
- [7] S. Ueno, A. Bagchi, S.L. Hakimi, and E.F. Schmeichel, "On Minimum Fault-Tolerant Networks," SIAM J. Disc. Mathematics, vol. 6, pp. 565-574, 1993.



Ting-Yi Sung received her PhD degree in operations research from New York University in 1989. She is currently an associate research fellow at the Institute of Information Science, Academia Sinica, Taiwan, Republic of China. Her research interests include mathematical programming, graph algorithms, and fault tolerance and architectures for interconnection networks.



Tung-Yang Ho received the BS and MS degrees in applied mathematics in 1984 and 1989, respectively, and the PhD degree in computer science in 1995, all from National Chiao Tung University, Taiwan, Republic of China. Currently, he is a post-doctoral researcher at the Institute of Information Science, Academia Sinica, Taiwan, Republic of China. His current research interests are in applications of graph theory to network architectures, CMOS layout, and graph algorithms.



Men-Yang Lin received his MS degree in applied mathematics from National Chung Hsing University in 1986. He is currently a lecturer at the Department of Information Management, National Taichung Institute of Commerce, Taiwan, Republic of China. His main research interests are in graph algorithms and interconnection networks.